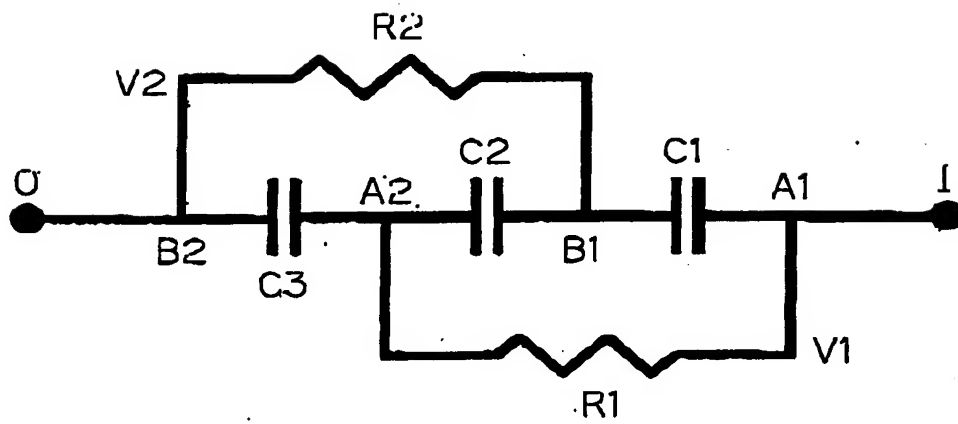


1



2

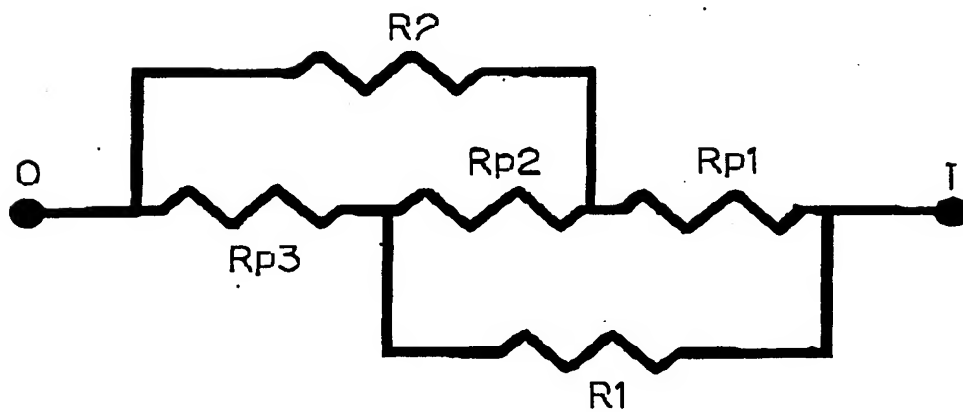


図 3

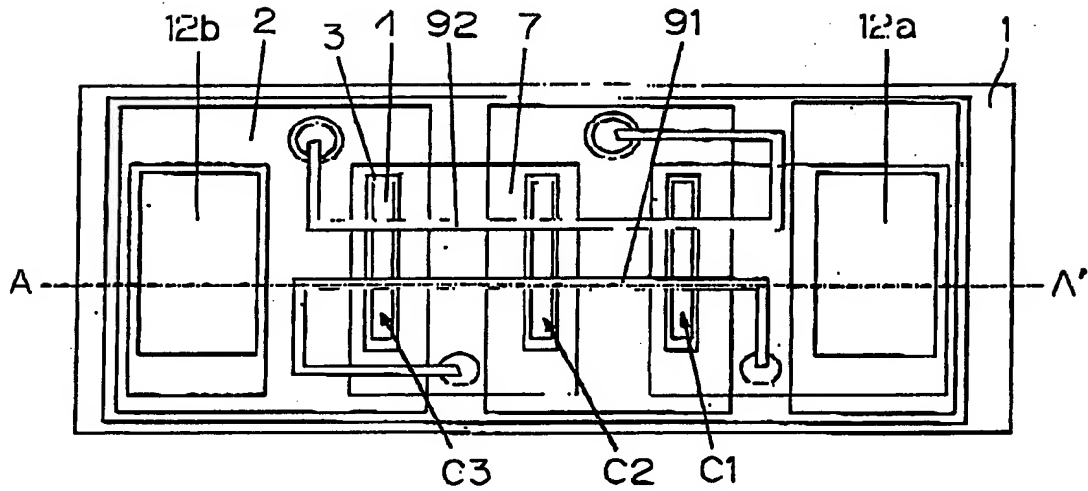


図 4

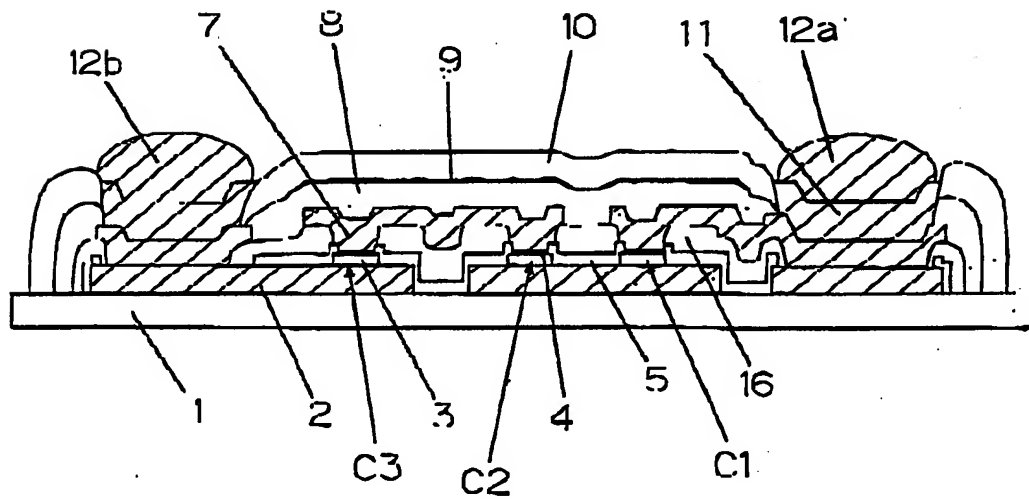


図 5

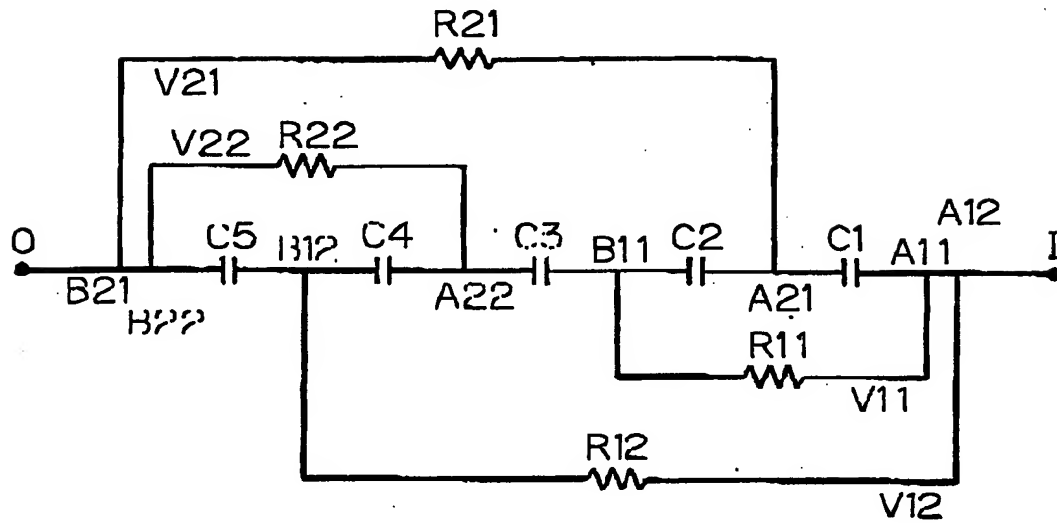


図 6

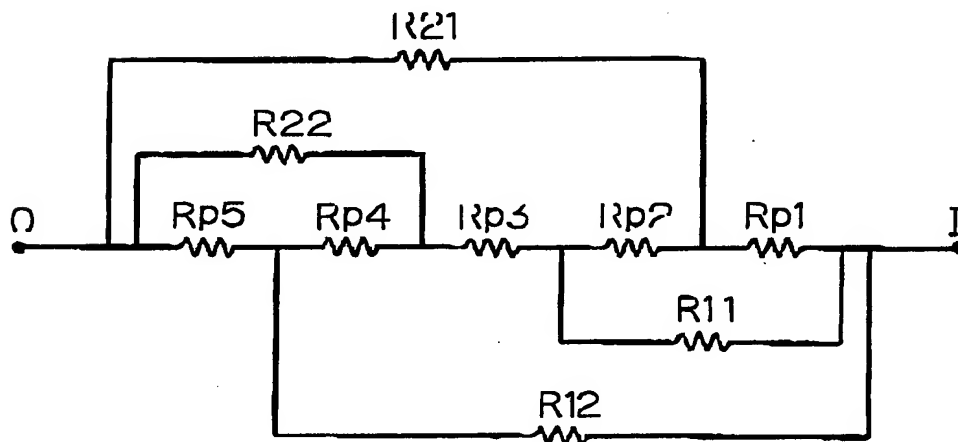


図 7

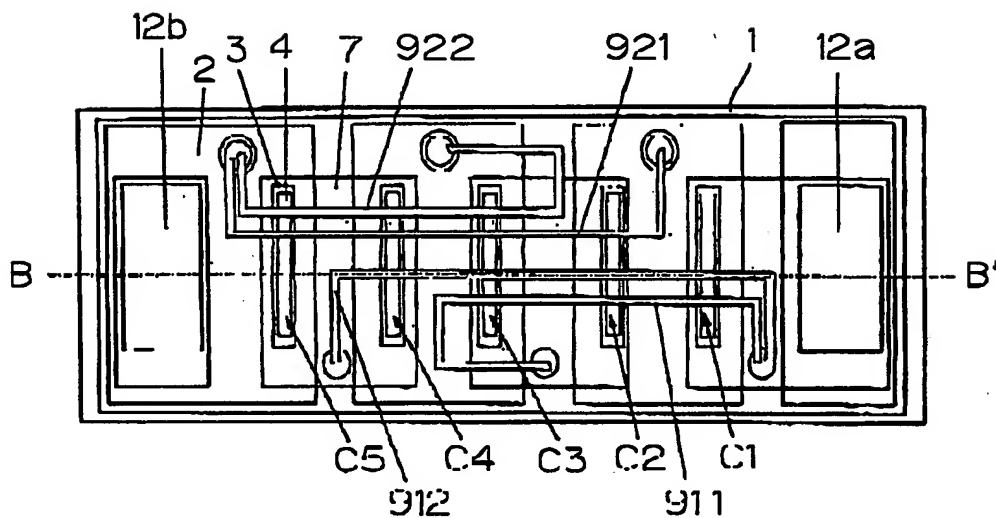


図 8

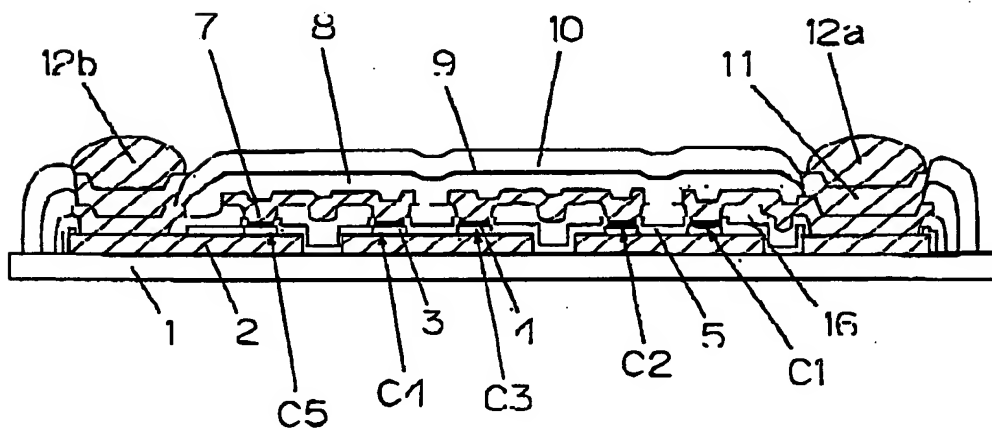


図 9

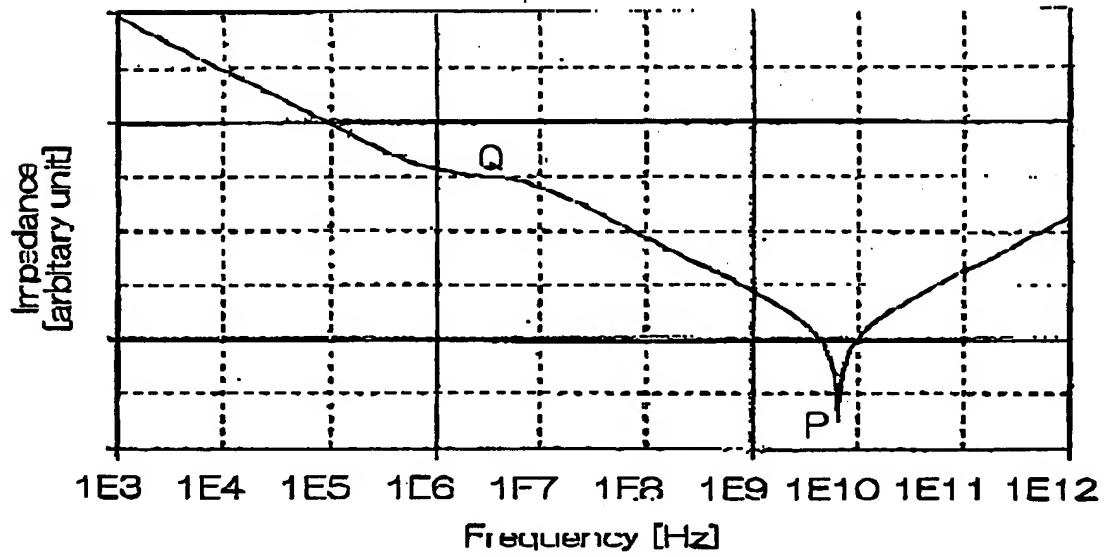


図 10

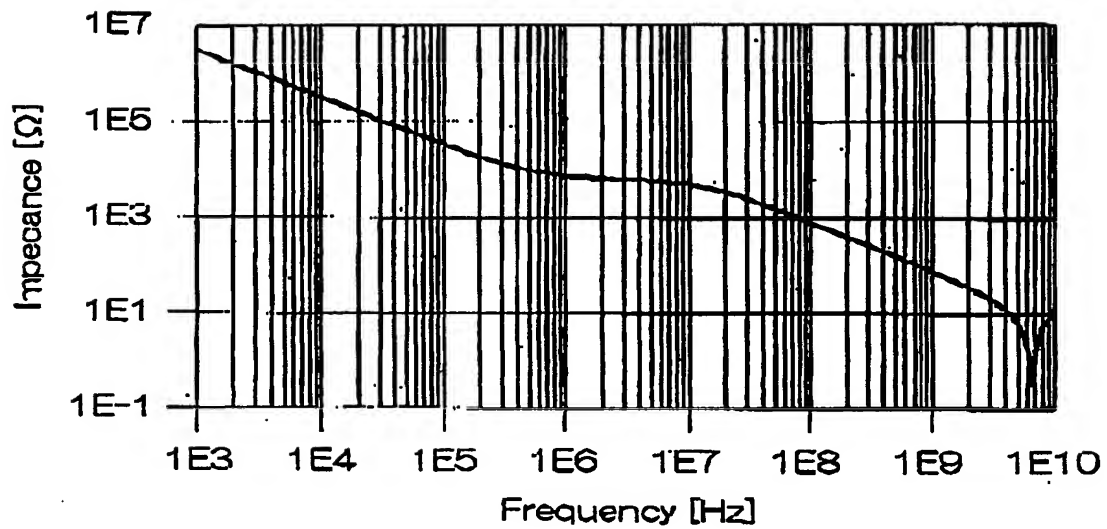


図 11

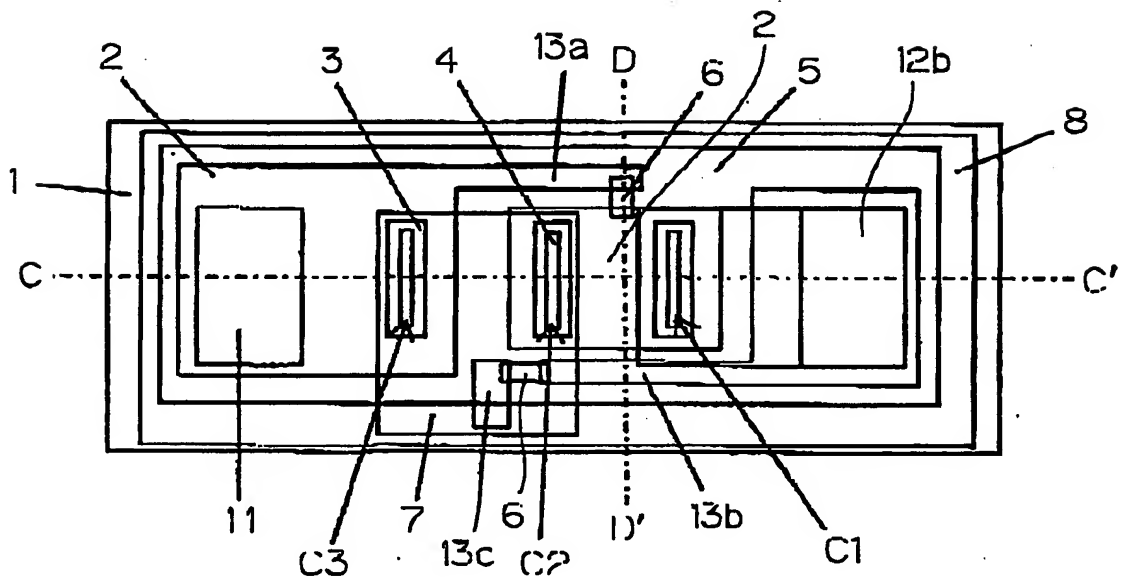


図 12

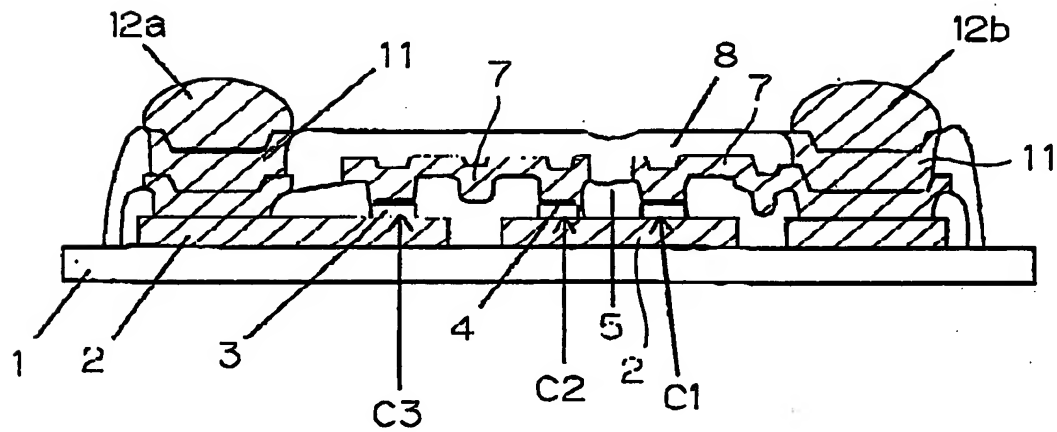


図 13

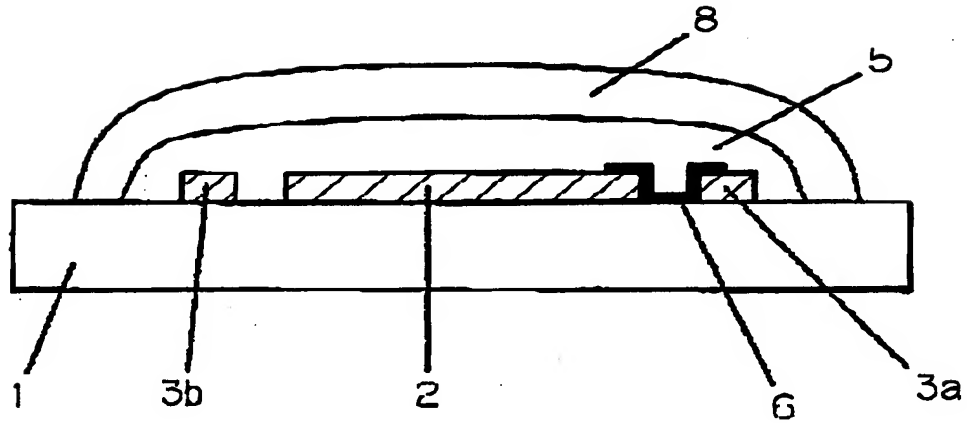


図 14

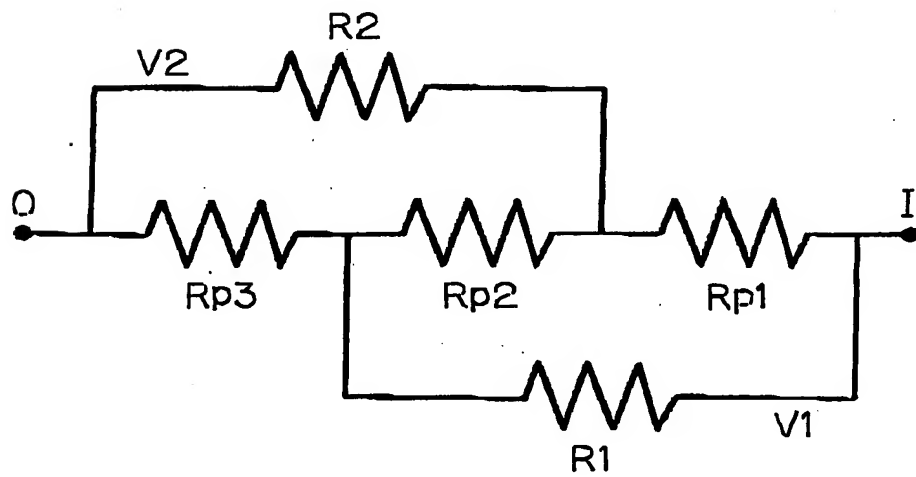


図 15

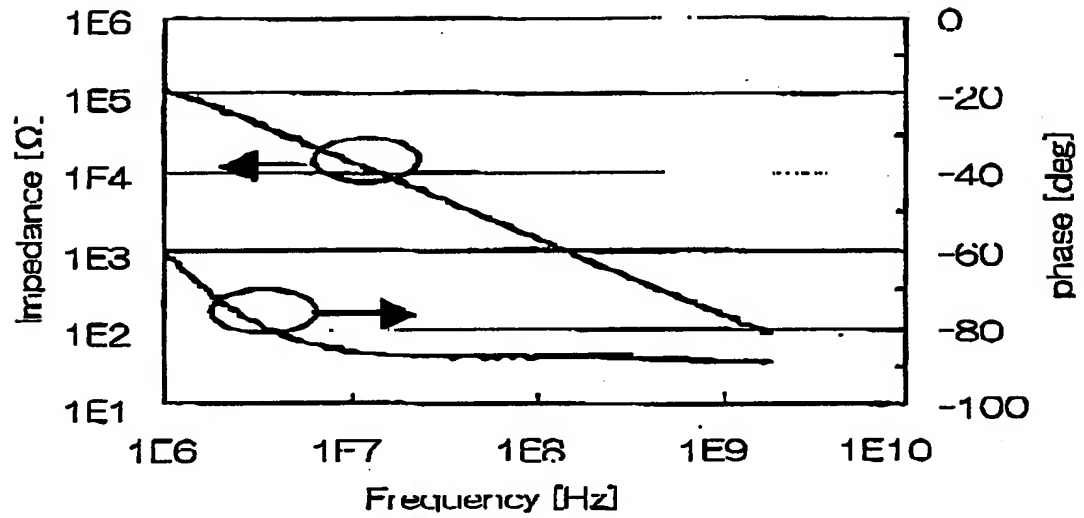


図 16

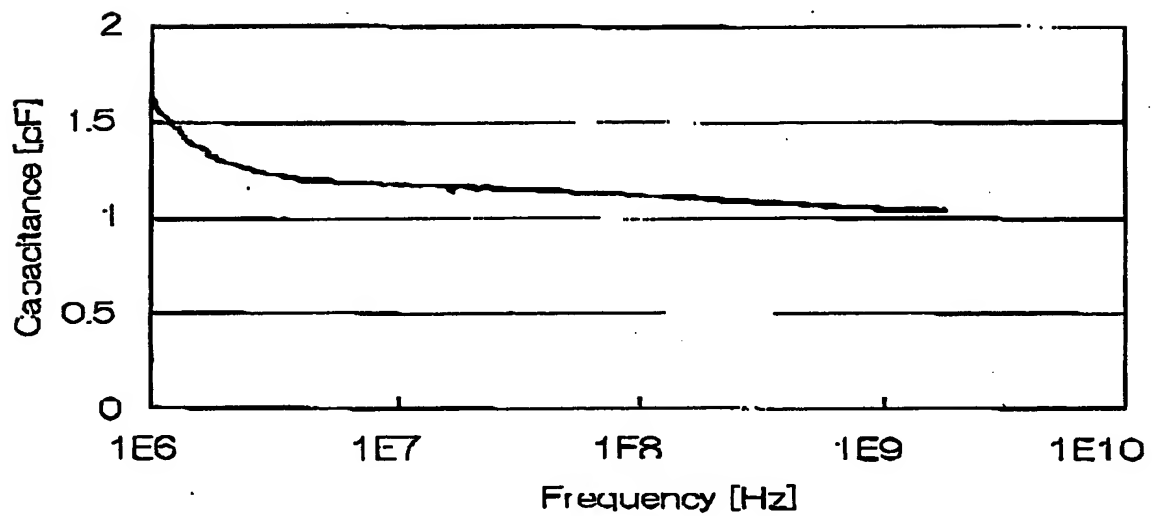




図 17

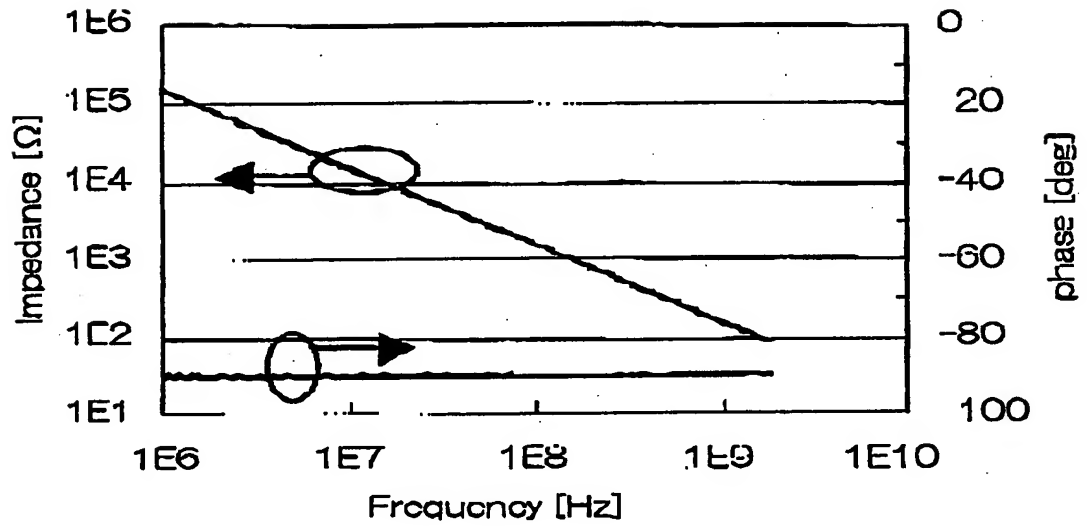
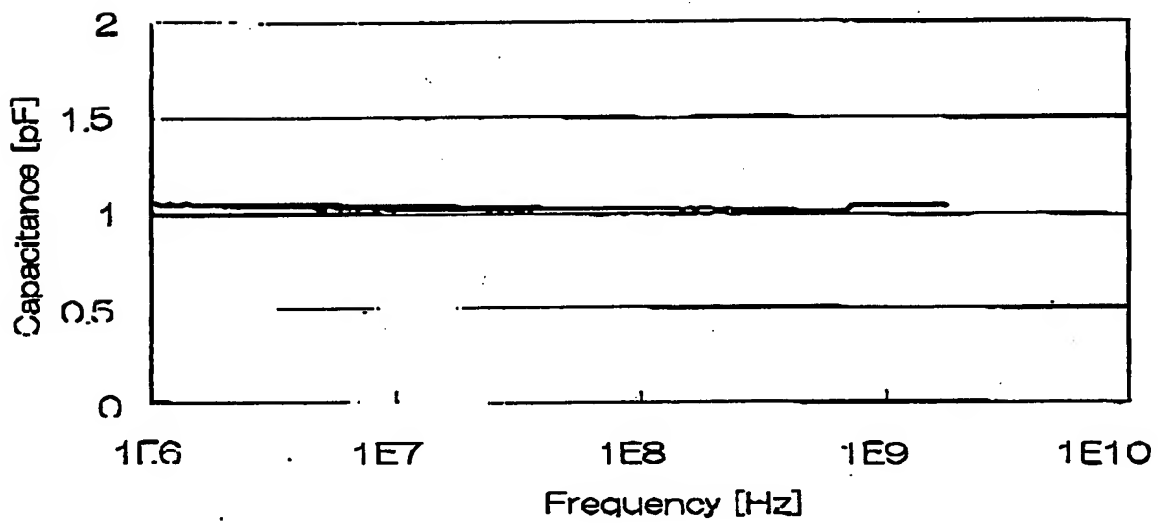


図 18



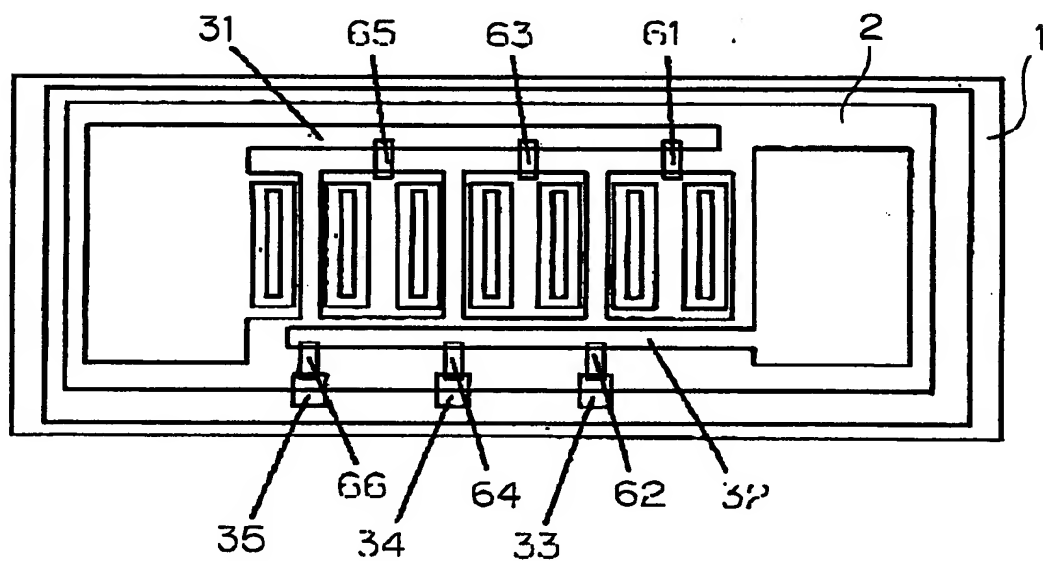
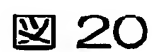


図 21

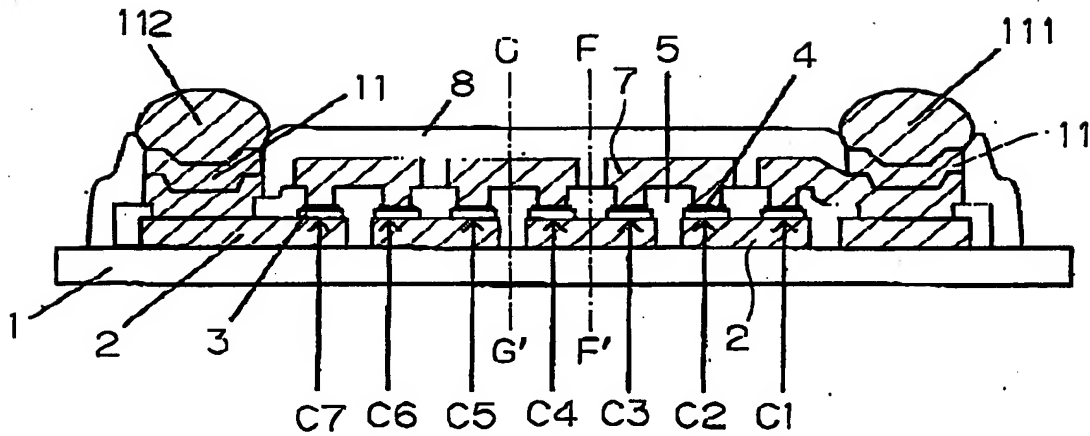
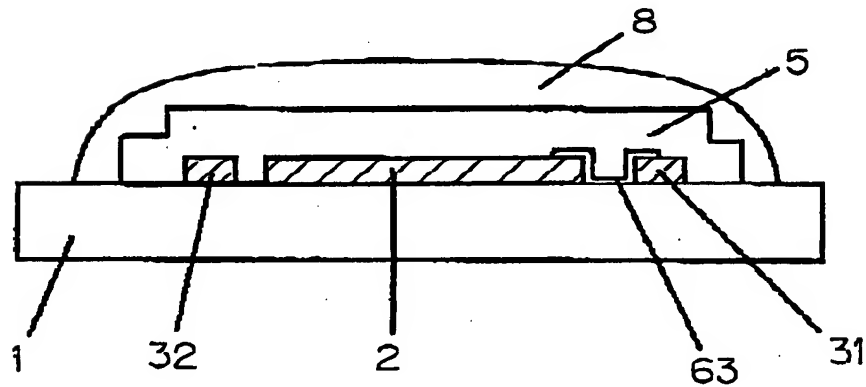
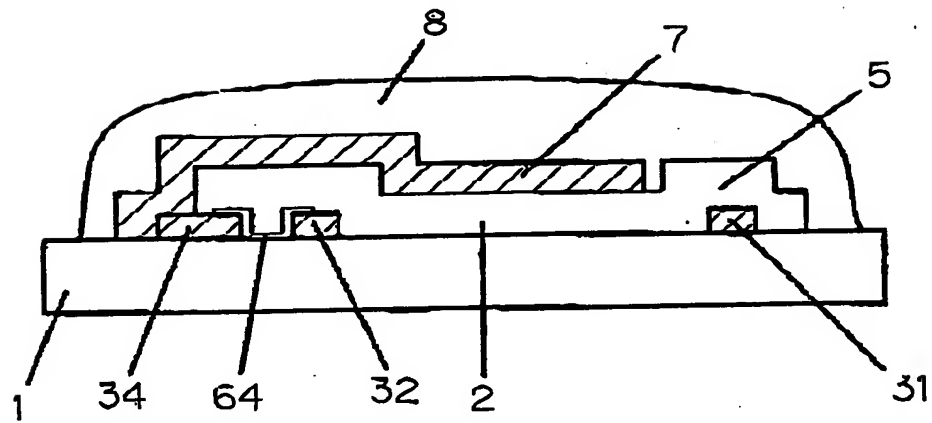


図 22



23



24

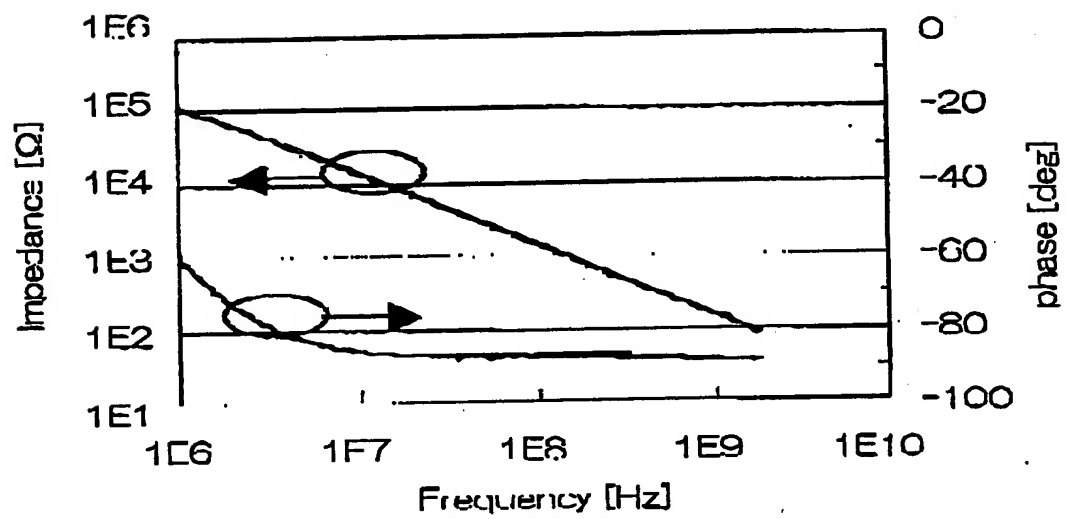


図 25

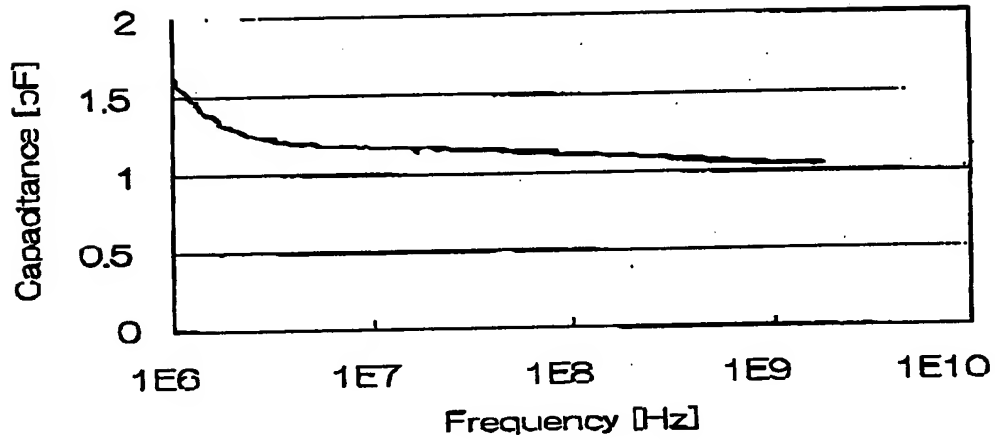
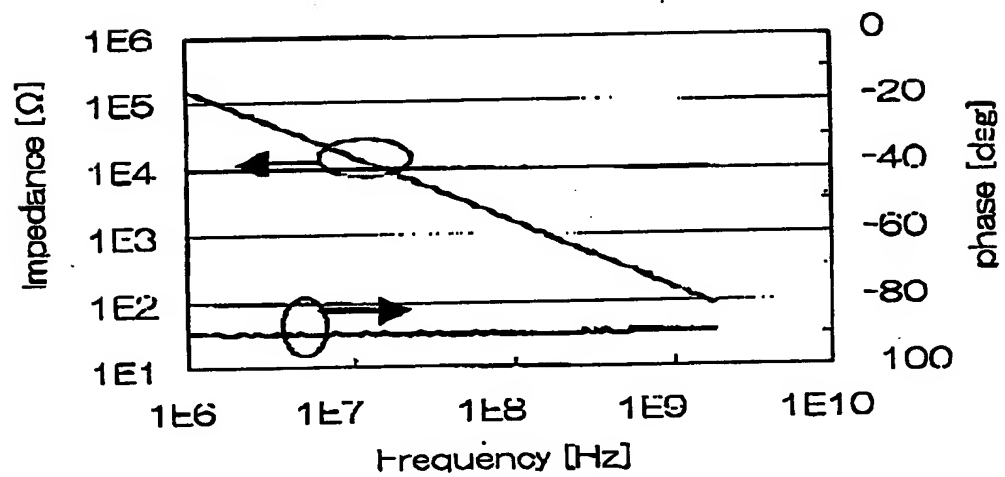


図 26



27

